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Simulation Methods for ESD Protection Development - Harald Gossner 2003-10-16

Simulation Methods for ESD Protection Development looks at the integration of new techniques into a comprehensive development flow, which is now available due advances made in the field during the recent years. These findings allow for an early, stable ESD concept at a very early stage of the technology development, which is essential now development cycles have been reduced. The book also offers ways of increasing the optimization and control of the technology concerning performance, thus making the process more cost effective and increasingly efficient. This title provides a guide through the latest research and technology presenting the ESD protection development methodology. This is based on a combination of process, device and circuit stimulation, and addresses the optimization of the industry critical issue, reduced development cycles. Written to address the needs of the ESD engineer, this text is required reading by all industry practitioners and researchers and students within this field. The FIRST Extensive overview on the subject of ESD simulation Addresses the industry critical issue of reduced development cycles, and provides solutions Presents the latest research in the field with high practical relevance and its results Electrostatic Discharge Protection - Juin J. Liou 2017-12-19

Electrostatic discharge (ESD) is one of the most prevalent threats to electronic components. In an ESD event, a finite amount of charge is transferred from one object (i.e., human body) to another (i.e., microchip). This process can result in a very high current passing through the microchip within a very short period of time. Thus, more than 35 percent of single-event chip damages can be attributed to ESD events, and designing ESD structures to protect integrated circuits against the ESD stresses is a high priority in the semiconductor industry. Electrostatic Discharge Protection: Advances and Applications delivers timely coverage of component- and system-level ESD protection for semiconductor devices and integrated circuits. Bringing together contributions from internationally respected researchers and engineers with expertise in ESD design, optimization, modeling, simulation, and characterization, this book bridges the gap between theory and practice to offer valuable insight into the state of the art of ESD protection. Amply illustrated with tables, figures, and case studies, the text: Instills a deeper understanding of ESD events and ESD protection design principles Examines vital processes including Si CMOS, Si BCD, Si SOI, and GaN technologies Addresses important aspects pertinent to the modeling and simulation of ESD protection solutions Electrostatic Discharge Protection: Advances and Applications provides a single source for cutting-edge information vital to the research and development of effective, robust ESD protection solutions for semiconductor devices and integrated circuits.

Portable Design - 2004

Inductive Sensors for Industrial Applications - Sorin Fericean 2018-12-31

This practical guide provides a comprehensive survey of all relevant inductive sensor classes for industrial applications in a single volume, from automotive use to white goods, covering design, fabrication, implementation, principles and functionality as well as standards and EMC requirements. The book addresses professional engineers and technicians, but is also accessible to students who require a solid basic knowledge of inductive sensors. Each chapter begins with classic, traditional explanations and gradually moves on to state-of-the-art analog and digital solutions, including large-scale integrated systems-on-chip, software defined sensors SDS, digital signal synthesis, coils on silicon and active inductors. The book employs three modern analysis methods: analytic computation; popular graphical methods (phasor diagrams, phase plans, Smith charts, etc.) and computer assisted tools, like the electromagnetic field simulator, Maxwell, and the popular Spice

simulator for electronic circuits. For traditional solutions, the chapters give overviews in tables with computation formulae (including empirical expressions). Numerical examples help the reader consolidate the theoretical knowledge gained. Concrete examples for currently available commercial parts are provided.

The ESD Handbook - Steven H. Voldman 2021-04-12

A practical and comprehensive reference that explores Electrostatic Discharge (ESD) in semiconductor components and electronic systems The ESD Handbook offers a comprehensive reference that explores topics relevant to ESD design in semiconductor components and explores ESD in various systems. Electrostatic discharge is a common problem in the semiconductor environment and this reference fills a gap in the literature by discussing ESD protection. Written by a noted expert on the topic, the text offers a topic-by-topic reference that includes illustrative figures, discussions, and drawings. The handbook covers a wide-range of topics including ESD in manufacturing (garments, wrist straps, and shoes); ESD Testing; ESD device physics; ESD semiconductor process effects; ESD failure mechanisms; ESD circuits in different technologies (CMOS, Bipolar, etc.); ESD circuit types (Pin, Power, Pin-to-Pin, etc.); and much more. In addition, the text includes a glossary, index, tables, illustrations, and a variety of case studies. Contains a well-organized reference that provides a quick review on a range of ESD topics Fills the gap in the current literature by providing information from purely scientific and physical aspects to practical applications Offers information in clear and accessible terms Written by the accomplished author of the popular ESD book series Written for technicians, operators, engineers, circuit designers, and failure analysis engineers, The ESD Handbook contains an accessible reference to ESD design and ESD systems.

Data Acquisition Systems - Maurizio Di Paolo Emilio 2013-03-21

This book describes the fundamentals of data acquisition systems, how they enable users to sample signals that measure real physical conditions and convert the resulting samples into digital, numeric values that can be analyzed by a computer. The author takes a problem-solving approach to data acquisition, providing the tools engineers need to use the concepts introduced. Coverage includes sensors that convert physical parameters to electrical signals, signal conditioning circuitry to convert sensor signals into a form that can be converted to digital values and analog-to-digital converters, which convert conditioned sensor signals to digital values. Readers will benefit from the hands-on approach, culminating with data acquisition projects, including hardware and software needed to build data acquisition systems.

EMI Protection for Communication Systems - Kresimir Malaric 2014-05-14

This practical reference provides a thorough understanding of how to protect communication systems from intentional and unintentional electromagnetic interference. You learn how to overcome critical problems in both digital and analog communications. This unique resource shows you how to shield equipment from electrical and magnetic fields, design TEM and GTEM-Cell, build capacitive coupling clamps for susceptibility tests, protect electronic equipment with filters, and calculate the measurement uncertainty. You find numerous, well-illustrated examples that make challenging electromagnetics issues far easier to comprehend.

Advances in Computation and Intelligence - Zhenhua Li 2009-09-30

Volumes CCIS 51 and LNCS 5812 constitute the proceedings of the Fourth International Symposium on Intelligence Computation and Applications, ISICA 2009, held in Huangshi, China, during October 23-25. ISICA 2009 attracted over 300 submissions. Through rigorous reviews, 58 papers were included in LNCS 5821, and 54 papers were collected in CCIS 51. ISICA conferences are one of the first series of international conferences on computational intelligence that combine

elements of learning, adaptation, evolution and fuzzy logic to create programs as alternative solutions to artificial intelligence.

Op Amp Applications Handbook - Walt Jung 2005

In the past several years, many advances have been made in operational amplifiers and the latest op amps have powerful new features, making them more suitable for use in many products requiring weak signal amplification, such as medical devices, communications technology, optical networks, and sensor interfacing. Walt Jung, analog design guru and author of the classic IC OP-Amp Cookbook (which has gone into three editions since 1974), has now written what may well be the ultimate op amp reference book. As Jung says, "This book is a compendium of everything that can currently be done with op amps."

This book is brimming with up-to-date application circuits, handy design tips, historical perspectives, and in-depth coverage of the latest techniques to simplify op amp circuit designs and improve their performance. There is a need for engineers to keep up with the many changes taking place in the new op amps coming onto the market, and to learn how to make use of the new features in the latest applications such as communications, sensor interfacing, manufacturing control systems, etc.. This book contains the answers and solutions to most of the problems that occur when using op amps in many different types of designs, by a very reputable and well-known author. Anything an engineer will want to know about designing with op amps can be found in this book. *Seven major sections packed with technical information *Anything an engineer will want to know about designing with op amps can be found in this book *This practical reference will be in great demand, as op amps is considered a difficult area in electronics design and engineers are always looking for help with it

LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers

- Paul Leroux 2006-03-30

LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers fits in the quest for complete CMOS integration of wireless receiver front-ends. With a combined discussion of both RF and ESD performance, it tackles one of the final obstacles on the road to CMOS integration. The book is conceived as a design guide for those actively involved in the design of CMOS wireless receivers. The book starts with a comprehensive introduction to the performance requirements of low-noise amplifiers in wireless receivers. Several popular topologies are explained and compared with respect to future technology and frequency scaling. The ESD requirements are introduced and related to the state-of-the-art protection devices and circuits. LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers provides an extensive theoretical treatment of the performance of CMOS low-noise amplifiers in the presence of ESD-protection circuitry. The influence of the ESD-protection parasitics on noise figure, gain, linearity, and matching are investigated. Several RF-ESD co-design solutions are discussed allowing both high RF-performance and good ESD-immunity for frequencies up to and beyond 5 GHz. Special attention is also paid to the layout of both active and passive components. LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers offers the reader intuitive insight in the LNA's behavior, as well as the necessary mathematical background to optimize its performance. All material is experimentally verified with several CMOS implementations, among which a fully integrated GPS receiver front-end. The book is essential reading for RF design engineers and researchers in the field and is also suitable as a text book for an advanced course on the subject.

Design and Development of Medical Electronic Instrumentation - David Prutchi 2005-01-28

Design and Development of Medical Electronic Instrumentation fills a gap in the existing medical electronic devices literature by providing background and examples of how medical instrumentation is actually designed and tested. The book includes practical examples and projects, including working schematics, ranging in difficulty from simple biopotential amplifiers to computer-controlled defibrillators. Covering every stage of the development process, the book provides complete coverage of the practical aspects of amplifying, processing, simulating and evoking biopotentials. In addition, two chapters address the issue of safety in the development of electronic medical devices, and providing valuable insider advice.

Testing for EMC Compliance - Mark I. Montrose 2004-04-08

The Keep It Simple (KISS) philosophy is the primary focus of this book. It is written in very simple language with minimal math, as a compilation of helpful EMI troubleshooting hints. Its light-hearted tone is at odds with the extreme seriousness of most engineering reference works that become boring after a few pages. This text tells engineers what to do and

how to do it. Only a basic knowledge of math, electronics, and a basic understanding of EMI/EMC are necessary to understand the concepts and circuits described. Once EMC troubleshooting is demystified, readers learn there are quick and simple techniques to solve complicated problems a key aspect of this book. Simple and inexpensive methods to resolve EMI issues are discussed to help generate unique ideas and methods for developing additional diagnostic tools and measurement procedures. An appendix on how to build probes is included. It can be a fun activity, even humorous at times with bizarre techniques (i.e., the sticky finger probe).

Electronic Engineering - 1996

IEICE Transactions on Electronics - 2008

IC Master - 2001

Building Embedded Systems - Changyi Gu 2016-05-26

Develop the software and hardware you never think about. We're talking about the nitty-gritty behind the buttons on your microwave, inside your thermostat, inside the keyboard used to type this description, and even running the monitor on which you are reading it now. Such stuff is termed embedded systems, and this book shows how to design and develop embedded systems at a professional level. Because yes, many people quietly make a successful career doing just that. Building embedded systems can be both fun and intimidating. Putting together an embedded system requires skill sets from multiple engineering disciplines, from software and hardware in particular. Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project: Programmers who know software will learn what they need to know about hardware. Engineers with hardware knowledge likewise will learn about the software side. Whatever your background is, Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices. Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems. He brings knowledge of numerous approaches to embedded systems design, including the System on Programmable Chips (SOPC) approach that is currently growing to dominate the field. His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field, or even just to do some embedded programming as a side project. What You Will Learn Program embedded systems at the hardware level Learn current industry practices in firmware development Develop practical knowledge of embedded hardware options Create tight integration between software and hardware Practice a work flow leading to successful outcomes Build from transistor level to the system level Make sound choices between performance and cost Who This Book Is For Embedded-system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware. Those who favor the System on a Programmable Chip (SOPC) approach will in particular benefit from this book. Students in both Electrical Engineering and Computer Science can also benefit from this book and the real-life industry practice it provides.

Cyber and Electromagnetic Threats in Modern Relay Protection -

Vladimir Gurevich 2017-12-19

Cyber and Electromagnetic Threats in Modern Relay Protection provides a detailed overview of the vulnerabilities of digital protection relays to natural and intentional destructive impacts, including cyber attacks and electromagnetic intrusions. From lightning strikes, electromagnetic fields generated by operating equipment, and issues with control cable shielding to modern technical tools that realize intentional destructive impacts remotely, this first-of-its-kind text covers the latest cyber and electromagnetic threats to digital protection relays. Emphasizing the importance of relay protection to the infrastructure of a country, this book: Explains how technological advances in the power industry, like the smart grid, can create dangerous vulnerabilities Discusses traditional passive means of protection, such as screened cabinets, filters, cables, special materials, and covers Describes advanced protective solutions based on hardware methods Cyber and Electromagnetic Threats in Modern Relay Protection is a valuable reference for engineers involved in the design, development, and use of relay protection. It is also beneficial for scientists, researchers, and students of vocational schools and technical universities.

High-/Mixed-Voltage Analog and RF Circuit Techniques for Nanoscale CMOS - Pui-In Mak 2012-03-20

This book presents high-/mixed-voltage analog and radio frequency (RF) circuit techniques for developing low-cost multistandard wireless receivers in nm-length CMOS processes. Key benefits of high-/mixed-voltage RF and analog CMOS circuits are explained, state-of-the-art examples are studied, and circuit solutions before and after voltage-conscious design are compared. Three real design examples are included, which demonstrate the feasibility of high-/mixed-voltage circuit techniques. Provides a valuable summary and real case studies of the state-of-the-art in high-/mixed-voltage circuits and systems; Includes novel high-/mixed-voltage analog and RF circuit techniques - from concept to practice; Describes the first high-voltage-enabled mobile-TVRF front-end in 90nm CMOS and the first mixed-voltage full-band mobile-TV Receiver in 65nm CMOS; Demonstrates the feasibility of high-/mixed-voltage circuit techniques with real design examples.

Electromagnetic Compatibility - David Weston 2017-12-19

This totally revised and expanded reference/text provides comprehensive, single-source coverage of the design, problem solving, and specifications of electromagnetic compatibility (EMC) into electrical equipment/systems-including new information on basic theories, applications, evaluations, prediction techniques, and practical diagnostic options for preventing EMI through cost-effective solutions. Offers the most recent guidelines, safety limits, and standards for human exposure to electromagnetic fields! Containing updated data on EMI diagnostic verification measurements, as well as over 900 drawings, photographs, tables, and equations-500 more than the previous edition-*Electromagnetic Compatibility: Principles and Applications, Second Edition*:

Electronic Design - 2007

McGraw-Hill Circuit Encyclopedia and Troubleshooting Guide - John D. Lenk 1993

Two books in one--and an unmatched resource for electronic circuit designers, technicians, students, and hobbyists worldwide. Not a revision but the latest in the series, this compendium combines the traditional circuit "cookbook" with state-of-the-art troubleshooting techniques. Thus it's the only reference that allows readers to build, test, and customize a wealth of useful circuit types. All 600+ pre-designed circuits are new (they do not appear in earlier volumes), and are conveniently grouped by function. While other circuit books present only basic schematics, Lenk's encyclopedias also explain how to put each circuit to work. Moreover, they provide debugging and troubleshooting routines to use when things don't go as planned, making it easy to integrate circuits into existing systems.

ESD - Steven H. Voldman 2006-11-02

With the growth of high-speed telecommunications and wireless technology, it is becoming increasingly important for engineers to understand radio frequency (RF) applications and their sensitivity to electrostatic discharge (ESD) phenomena. This enables the development of ESD design methods for RF technology, leading to increased protection against electrical overstress (EOS) and ESD. *ESD: RF Technology and Circuits*: Presents methods for co-synthesizing ESD networks for RF applications to achieve improved performance and ESD protection of semiconductor chips; discusses RF ESD design methods of capacitance load transformation, matching network co-synthesis, capacitance shunts, inductive shunts, impedance isolation, load cancellation methods, distributed loads, emitter degeneration, buffering and ballasting; examines ESD protection and design of active and passive elements in RF complementary metal-oxide-semiconductor (CMOS), RF laterally-diffused metal oxide semiconductor (LDMOS), RF BiCMOS Silicon Germanium (SiGe), RF BiCMOS Silicon Germanium Carbon (SiGeC), and Gallium Arsenide technology; gives information on RF ESD testing methodologies, RF degradation effects, and failure mechanisms for devices, circuits and systems; highlights RF ESD mixed-signal design integration of digital, analog and RF circuitry; sets out examples of RF ESD design computer aided design methodologies; covers state-of-the-art RF ESD input circuits, as well as voltage-triggered to RC-triggered ESD power clamps networks in RF technologies, as well as off-chip protection concepts. Following the authors series of books on ESD, this book will be a thorough overview of ESD in RF technology for RF semiconductor chip and ESD engineers. Device and circuit engineers working in the RF domain, and quality, reliability and failure analysis engineers will also find it a valuable reference in the rapidly growing area of RF ESD design. In addition, it will appeal to graduate students in RF microwave technology and RF circuit design.

Microelectronics Technology and Devices--SBMICRO 2007 - João Antonio

Martino 2007

The SBMicro symposium is a forum dedicated to fabrication and modeling of Microsystems, integrated circuits and devices. The goal of the symposium is to bring together researchers in the areas of processing, materials, characterization, modeling and TCAD of integrated circuits, microsensors, microactuators, and MEMS. This issue contains the papers presented at the 2007 conference.

ESD Protection Device and Circuit Design for Advanced CMOS Technologies - Oleg Semenov 2008-04-26

ESD Protection Device and Circuit Design for Advanced CMOS Technologies is intended for practicing engineers working in the areas of circuit design, VLSI reliability and testing domains. As the problems associated with ESD failures and yield losses become significant in the modern semiconductor industry, the demand for graduates with a basic knowledge of ESD is also increasing. Today, there is a significant demand to educate the circuits design and reliability teams on ESD issues. This book makes an attempt to address the ESD design and implementation in a systematic manner. A design procedure involving device simulators as well as circuit simulator is employed to optimize device and circuit parameters for optimal ESD as well as circuit performance. This methodology, described in *ESD Protection Device and Circuit Design for Advanced CMOS Technologies* has resulted in several successful ESD circuit design with excellent silicon results and demonstrates its strengths.

EDN - 2007

Embedded Systems Design using the Rabbit 3000 Microprocessor - Kamal Hyder 2005-03-30

The Rabbit 3000 is a popular high-performance microprocessor specifically designed for embedded control, communications, and Ethernet connectivity. This new technical reference book will help designers get the most out of the Rabbit's powerful feature set. The first book on the market to focus exclusively on the Rabbit 3000, it provides detailed coverage of: Rabbit architecture and development environment, interfacing to the external world, networking, Rabbit assembly language, multitasking, debugging, Dynamic C and much more! Authors Kamal Hyder and Bob Perrin are embedded engineers with years of experience and they offer a wealth of design details and "insider" tips and techniques. Extensive embedded design examples are supported by fully tested source code. Whether you're already working with the Rabbit or considering it for a future design, this is one reference you can't be without! Let the experts teach you how to design embedded systems that efficiently hook up to the Internet using networked core modules. Provides a number of projects and source code using RabbitCore, which will make it easy for the system designer and programmer to get hands-on experience developing networked devices

42 V-PowerNets - Henning Wallentowitz 2011-06-27

The efficiency of today's automobile PowerNets is limited and widely exhausted and the introduction of new systems is becoming more and more difficult. An offered remedy is the introduction of a new voltage level of 42 V. Within the scope of the EUROMOTOR symposium, associated problems were discussed and special methods for the solution were presented. The book describes current as well as futuristic fusing and protection concepts of vehicle PowerNets. It presents the introduction of a new 42 V-PowerNet into automobiles, the description of components and fusing systems for wires and electrical loads, the treatment of problems such as short-circuits and arcs, as well as seizing preventive measures. By focusing on the substantial aspects, the book addresses developers such as practitioners, who are active with automobile suppliers and vehicle manufacturers. Companies occupied with fusing devices, semiconductors, trunk circuits and electrical and/or electronic consumers will find this book especially interesting.

CMOS - MEMS - Henry Baltes 2013-03-26

This edition of 'CMOS-MEMS' was originally published in the successful series 'Advanced Micro & Nanosystems'. Here, the combination of the globally established, billion dollar chip mass fabrication technology CMOS with the fascinating and commercially promising new world of MEMS is covered from all angles. The book introduces readers to this field and takes them from fabrication technologies and material characterization aspects to the actual applications of CMOS-MEMS - a wide range of miniaturized physical, chemical and biological sensors and RF systems. Vital knowledge on circuit and system integration issues concludes this in-depth treatise, illustrating the advantages of combining CMOS and MEMS in the first place, rather than having a hybrid solution.

DC Power Supplies - Nihal Kularatna 2018-10-03

As we increasingly use electronic devices to direct our daily lives, so grows our dependence on reliable energy sources to power them. Because modern electronic systems demand steady, efficient, reliable DC voltage sources—often at a sub-1V level—commercial AC lines, batteries, and other common resources no longer suffice. New technologies also require intricate techniques to protect against natural and manmade disasters. Still, despite its importance, practical information on this critical subject remains hard to find. Using simple, accessible language to balance coverage of theoretical and practical aspects, *DC Power Supplies, Power Management and Surge Protection* details the essentials of power electronics circuits applicable to low-power systems, including modern portable devices. A summary of underlying principles and essential design points, it compares academic research and industry publications and reviews DC power supply fundamentals, including linear and low-dropout regulators. Content also addresses common switching regulator topologies, exploring resonant conversion approaches. Coverage includes other important topics such as: Control aspects and control theory Digital control and control ICs used in switching regulators Power management and energy efficiency Overall power conversion stage and basic protection strategies for higher reliability Battery management and comparison of battery chemistries and charge/discharge management Surge and transient protection of circuits designed with modern semiconductors based on submicron dimension transistors This specialized design resource explores applicable fundamental elements of power sources, with numerous cited references and discussion of commercial components and manufacturers. Regardless of their previous experience level, this information will greatly aid designers, researchers, and academics who, study, design, and produce the viable new power sources needed to propel our modern electronic world. CRC Press Authors Speak Nihal Kularatna introduces his book. Watch the video

[System Level ESD Protection](#) - Vladislav Vashchenko 2014-03-21

This book addresses key aspects of analog integrated circuits and systems design related to system level electrostatic discharge (ESD) protection. It is an invaluable reference for anyone developing systems-on-chip (SoC) and systems-on-package (SoP), integrated with system-level ESD protection. The book focuses on both the design of semiconductor integrated circuit (IC) components with embedded, on-chip system level protection and IC-system co-design. The readers will be enabled to bring the system level ESD protection solutions to the level of integrated circuits, thereby reducing or completely eliminating the need for additional, discrete components on the printed circuit board (PCB) and meeting system-level ESD requirements. The authors take a systematic approach, based on IC-system ESD protection co-design. A detailed description of the available IC-level ESD testing methods is provided, together with a discussion of the correlation between IC-level and system-level ESD testing methods. The IC-level ESD protection design is demonstrated with representative case studies which are analyzed with various numerical simulations and ESD testing. The overall methodology for IC-system ESD co-design is presented as a step-by-step procedure that involves both ESD testing and numerical simulations.

EDN, Electrical Design News - 2004

Silicon-on-insulator Technology and Devices XI - Electrochemical Society. Meeting 2003

Electromagnetic Compatibility - David A. Weston 2016-11-03

Revised, updated, and expanded, *Electromagnetic Compatibility: Methods, Analysis, Circuits, and Measurement*, Third Edition provides comprehensive practical coverage of the design, problem solving, and testing of electromagnetic compatibility (EMC) in electrical and electronic equipment and systems. This new edition provides novel information on theory, applications, evaluations, electromagnetic computational programs, and prediction techniques available. With sixty-nine schematics providing examples for circuit level electromagnetic interference (EMI) hardening and cost effective EMI problem solving, this book also includes 1130 illustrations and tables. Including extensive data on components and their correct implementation, the myths, misapplication, misconceptions, and fallacies that are common when discussing EMC/EMI will also be addressed and corrected.

On-Chip Electro-Static Discharge (ESD) Protection for Radio-Frequency Integrated Circuits - Qiang Cui 2015-03-10

This book enables readers to design effective ESD protection solutions for all mainstream RF fabrication processes (GaAs pHEMT, SiGe HBT, CMOS). The new techniques introduced by the authors have much higher

protection levels and much lower parasitic effects than those of existing ESD protection devices. The authors describe in detail the ESD phenomenon, as well as ESD protection fundamentals, standards, test equipment, and basic design strategies. Readers will benefit from realistic case studies of ESD protection for RFICs and will learn to increase significantly modern RFICs' ESD safety level, while maximizing RF performance.

ESD Protection Methodologies - Marise Bafleur 2017-07-26

Failures caused by electrostatic discharges (ESD) constitute a major problem concerning the reliability and robustness of integrated circuits and electronic systems. This book summarizes the many diverse methodologies aimed at ESD protection and shows, through a number of concrete studies, that the best approach in terms of robustness and cost-effectiveness consists of implementing a global strategy of ESD protection. *ESD Protection Methodologies* begins by exploring the various normalized test techniques that are used to qualify ESD robustness as well as characterization and defect localization methods aimed at implementing corrective measures. Due to the increasing complexity of integrated circuits, it is important to be able to provide a simulation in which the implemented ESD protection strategy provides the desired protection, while not harming the performance levels of the circuit. Therefore, the main features and difficulties related to the different types of simulation, finite element, SPICE-type and behavioral, are then studied. To conclude, several case studies are presented which provide real-life examples of the approaches explained in the previous chapters and validate a number of the strategies from component to system level. Provides a global ESD protection approach from component to system, including both the proposal of investigation techniques and predictive simulation methodologies Addresses circuit and system designers as well as failure analysis engineers Provides the description of specifically developed investigation techniques and the application of the proposed methodologies to real case studies

Analog Circuit Design - Arthur H.M. van Roermund 2007-05-08

Number 12 in the successful series of *Analog Circuit Design* provides valuable information and excellent overviews of analogue circuit design, CAD and RF systems. The series is an ideal reference for those involved in analogue and mixed-signal design.

Newark Electronics - 2009

A Microcontroller Based Control System for a Split Parallel Hybrid Electric Vehicle - Tareq Nisar Aryne 2001

Practical ESD Protection Design - Albert Wang 2022-01-06

An authoritative single-volume reference on the design and analysis of ESD protection for ICs Electrostatic discharge (ESD) is a major reliability challenge to semiconductors, integrated circuits (ICs), and microelectronic systems. On-chip ESD protection is a vital to any electronic products, such as smartphones, laptops, tablets, and other electronic devices. *Practical ESD Protection Design* provides comprehensive and systematic guidance on all major aspects of designs of on-chip ESD protection for integrated circuits (ICs). Written for students and practicing engineers alike, this one-stop resource covers essential theories, hands-on design skills, computer-aided design (CAD) methods, characterization and analysis techniques, and more on ESD protection designs. Detailed chapters examine an array of topics ranging from fundamental to advanced, including ESD phenomena, ESD failure analysis, ESD testing models, ESD protection devices and circuits, ESD design layout and technology effects, ESD design flows and co-design methods, ESD modelling and CAD techniques, and future ESD protection concepts. Based on the author's decades of design, research and teaching experiences, *Practical ESD Protection Design*:
• Features numerous real-world ESD protection design examples
• Emphasizes on ESD protection design techniques and procedures
• Describes ESD-IC co-design methodology for high-performance mixed-signal ICs and broadband radio-frequency (RF) ICs
• Discusses CAD-based ESD protection design optimization and prediction using both Technology and Electrical Computer-Aided Design (TCAD/ECAD) simulation
• Addresses new ESD CAD algorithms and tools for full-chip ESD physical design verification
• Explores the disruptive future outlook of ESD protection
Practical ESD Protection Design is a valuable reference for industrial engineers and academic researchers in the field, and an excellent textbook for electronic engineering courses in semiconductor microelectronics and integrated circuit designs.

[Electronic Products Magazine](#) - 1996

